CS 271 Computer Architecture and Assembly Language Course Calendar* Fall 2017

*Assignments are due end-of-week Sunday 11:59pm unless otherwise specified. Schedule subject to change based on material pace.

Unit / Week	Topics
#0:	Introductions
Program #1	
Week 0 Quiz	 Programming languages Virtual machines
Week 0 Quiz	
Week 0 Quiz	Computer architectures, processor types, metrics
Week o Quiz	Machine instructions, instruction execution cycle
	• CISC, x86 architectures, Intel IA-32 architecture
	• Introduction to MASM assembly language.
	Read Irvine Chapter 1
	Chapter 2.1, 2.2, 2.3
	Chapter 3.1, 3.2, 3.3 (pg 71 only), 3.4, 3.5
# <mark>1</mark> :	MASM assembly language:
	 Constants, variables
Week 1 Quiz	 Libraries, assembling, linking, loading
Program #2	 Addressing modes
	 Arithmetic operations
Week 1 Quiz	 Conditions, decisions, repetition
Program #1	
	Re-read Irvine Chapter 1.3, 1.4
	Read Irvine Chapter 4.1, 4.2, 4.5 (and 6.3)
#2:	MASM assembly language:
	 Modular development
Week 2 Quiz	 Data validation
	o Debugging
Week 2 Quiz	Internal/external data representation
Program #2	
	Read Irvine Chapter 5.1, 5.2, 5.3, 5.4, 5.6, 5.7
#3:	Binary arithmetic
	Floating-point representation
Week 3 Quiz	• Parity, error detection/correction, Hamming codes
Program #3	
	Read Irvine Chapter 6.1, 6.2, 6.3,
Week 3 Quiz	Chapter 7.3
	Chapter 12.1
#4:	MASM procedures:
	o Calls/returns
Week 4 Quiz	 Functional decomposition, parameters
Program #4	• Documentation
	MASM assembly language:
Week 4 Quiz	• The system stack
Program #3	• Parameter passing
	Read Irvine Chapter 4.4
	Read Irvine Chapter 8.1, 8.2

New Assignments are in BLACK. Due Assignments are in RED.

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	Monday 2:00pm in normal classroom
#11: Finals Week	Final Exam
#9-10: Week 9-10 Quiz Week 9-10 Quiz Program #6	 Parallelism Advanced architectures Review for final exam
#8: Week 8 Quiz Week 8 Quiz	 Recursion MASM assembly language: Macros String processing Digital logic level: Gates, circuits, integrated circuits
#7: Week 7 Quiz Program #6 Week 7 Quiz Program #5	 MASM assembly language: Data-related operators Low-level I/O RPN IA-32 floating-point unit (FPU) Read Irvine Chapter 9.1, 9.2, 9.4, 9.5 Re-read Irvine Chapter 12.1
 #5: Midterm Exam Program #4 Midterm Exam #6: Week 6 Quiz Program #5 Week 6 Quiz 	Review for Midterm Exam Midterm Exam Thursday in class MASM assembly language: MASM assembly language: More parameter passing Random numbers Arrays, array parameters Read Irvine Chapter 9.5